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Title of Invention:	Intelligent data storage and processing using fpga devices			
First Named Inventor/Applicant Name:	Roger D. Chamberlain			
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	IDS2.par	850317	no	5
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Warnings:

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Information:		Total Files Size (in bytes)	1		
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8	NPL Documents	ZIVCompression 1.pdf	1927877	no	
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Warnings:			_		
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6	NPL Documents	WOforPCTUS200416398.pdf	809718 43a32d58a33046c2600144c8938783265be	no	
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Warnings:					
5	NPL Documents	WOforPCTUS200416021.pdf	361073 f9d405157afaf7dcd02dbf8b832984ae6927 cS2f	no	
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Warnings:					
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4	NPL Documents	WESTAnFPGABasedSearch.PDF	9995363	no	
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Warnings:		I			
3	NPL Documents	WARD.pdf	ed5ebddab26210cbbb061f898df837f7e6d b244e	no	
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Warnings:		<u> </u>	9ae8		
2	NPL Documents	THOMPSON.pdf	c124601372e24f15fc91d2dff43680785031	no	
1			974848	J	

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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.